

## WEST Search History

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DATE: Thursday, May 27, 2004

**Hide? Set Name Query**

**Hit Count**

*DB=PGPB,USPT; PLUR=YES; OP=ADJ*

<input type="checkbox"/> L35	L34.ti,ab.	7
<input type="checkbox"/> L34	clock near10 ((integrat\$ or combin\$) near3 decod\$)	281
<input type="checkbox"/> L33	clock domain near10 ((integrat\$ or combin\$) near3 decod\$)	1
<input type="checkbox"/> L32	clock domain	1521
<input type="checkbox"/> L31	L30.ti.	0
<input type="checkbox"/> L30	L29 near5 decod\$	334
<input type="checkbox"/> L29	(fifo or buffer) near5 (synchroniz\$ or match\$)	12315

*DB=USPT; PLUR=YES; OP=ADJ*

<input type="checkbox"/> L28	L27.ti,ab.	2
<input type="checkbox"/> L27	decod\$ near3 on\$fly	81
<input type="checkbox"/> L26	decod\$ near3 no\$fly	0
<input type="checkbox"/> L25	L22 near5 domain	3
<input type="checkbox"/> L24	L23.ti.	21
<input type="checkbox"/> L23	decod\$ near3 (simultan\$ or concurrent\$ or while)	3822
<input type="checkbox"/> L22	decod\$ near5 (simultan\$ or concurrent\$ or while)	5178
<input type="checkbox"/> L21	L20.ti,ab.	1
<input type="checkbox"/> L20	decod\$ near5 (simultan\$ or concurrent\$ or while) near5 (transfer\$)	82
<input type="checkbox"/> L19	L18 not l12	59
<input type="checkbox"/> L18	fifo same ((integrat\$ or combin\$) near3 decod\$)	73
<input type="checkbox"/> L17	fifo near5 (compound\$ near3 decod\$)	0
<input type="checkbox"/> L16	fifo near5 (compound.\$ near3 decod\$)	0
<input type="checkbox"/> L15	fifo near5 (embed\$ near3 decod\$)	0
<input type="checkbox"/> L14	fifo near5 (embedd\$ near3 decod\$)	0
<input type="checkbox"/> L13	L12 not l3	6
<input type="checkbox"/> L12	fifo near10 ((integrat\$ or combin\$) near3 decod\$)	14
<input type="checkbox"/> L11	L10.ab.	8
<input type="checkbox"/> L10	(fifo or buffer) near5 ((integrat\$ or combin\$) near3 decod\$)	198
<input type="checkbox"/> L9	L7.ti.	15
<input type="checkbox"/> L8	L7.ti,ab.	63
<input type="checkbox"/> L7	(fifo or buffer or memory) near5 ((integrat\$ or combin\$) near3 decod\$)	768
<input type="checkbox"/> L6	l1 near10 decod\$	21

<input type="checkbox"/>	L5	decod\$ near5 (simultan\$ or concurrent\$) near10 fifo	35
<input type="checkbox"/>	L4	4755817.pn.	1
<input type="checkbox"/>	L3	fifo near5 ((integrat\$ or combin\$) near3 decod\$)	8
<input type="checkbox"/>	L2	L1 near5 ((integrat\$ or combin\$) near3 decod\$)	0
<input type="checkbox"/>	L1	fifo near5 synchroniz\$	1006

END OF SEARCH HISTORY

First Hit    Fwd Refs 

L3: Entry 1 of 8

File: USPT

Jun 13, 2000

DOCUMENT-IDENTIFIER: US 6076062 A

TITLE: Method and device for transferring and decoding a non-PCM bitstream between a digital video disc and a multi-channel reproduction apparatus

Detailed Description Text (23):

Like the DVD player, MC.sub.-- box 46 has an internal control path 41, interface circuit 38 of type MSM6307, and control processing in microprocessor 40. In correspondence to FIFO 28, the MC.sub.-- box 46 has a relatively small FIFO 44. This stores the data of one bitstream while the previous one is decoded locally. The decoding pertains first to the burst level, and next to the sample level. The output from FIFO 44 feeds MC.sub.-- decoder 42 that may output up to seven audio channels as indicated: Left, Right, LFE/C, Left center surround, Right center surround, Left surround and Right surround. As shown, these are grouped on four I2S interfaces, according to a protocol described in U.S. Pat. No. 4,755,817 to the present assignee. Alternatively FIFO 44 plus decoder 42 are combined into a single hardware block and controlled directly by the commands contained in the IEC 958 data. Moreover, the MC.sub.-- Box attaches to the secondary control channel 48 by means of circuit 38.